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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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|----------|-------------|-------|----------|------|-------------|
| APPL NUM | FILING DATE | CLASS | SUBCLASS | CAU | EXAMINER |
| 10061504 | 02/01/2002 | 327 | 295 | 2816 | Linh Nguyen |

**APPLICANTS: Neff Robert:

**CONTINUING DATA VERIFIED: *LN*

**FOREIGN APPLICATIONS VERIFIED: *LN*

| | | |
|--|--|--------------------|
| PUBLISHED DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> | ATTORNEY DOCKET NO |
| Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | 10010205-1 |
| Verified and Acknowledged Examiner's initials <i>LN</i> | | |

TITLE: Interleaved clock signal generator having serial delay and ring counter architecture

U.S. DEPT. OF COMM. / PAT. & TM. PTO-435 (Rev. 11-94)

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|---|-----------|---------------------------|-------------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheet Drawg. | Figs. Drawg. Print Fig. |
| | | Application Examiner | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | |
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